This Page Is Inserted by IFW Operations and is not a part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

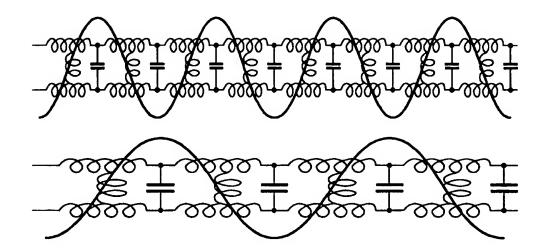
IMAGES ARE BEST AVAILABLE COPY.

As rescanning documents will not correct images, please do not report the images to the Image Problem Mailbox.

App No.: Not Yet Assigned Do Inventor: Kanji OTSUKA, et al.
Title: ELECTRONIC CIRCUIT DEVICE Docket No.: 492322017000

1/22

FIG.1



App No.: Not Yet Assigned Do Inventor: Kanji OTSUKA, et al.
Title: ELECTRONIC CIRCUIT DEVICE

Docket No.: 492322017000

2/22

FIG.2

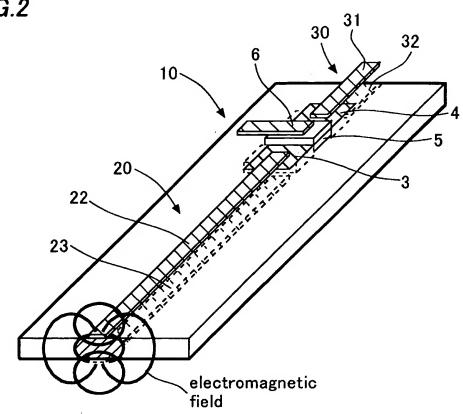
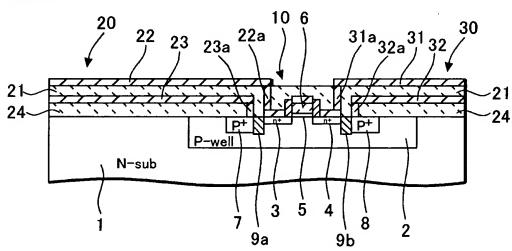


FIG.3

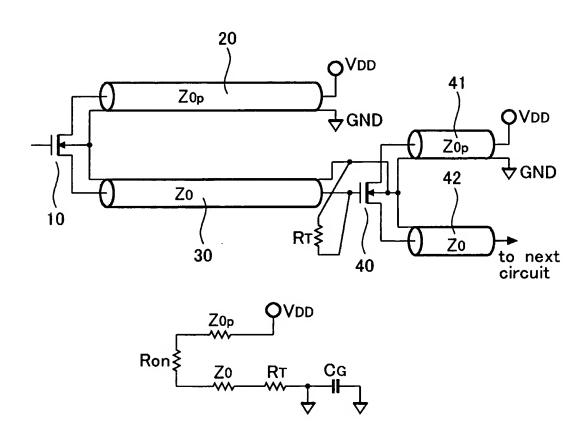


App No.: Not Yet Assigned Do Inventor: Kanji OTSUKA, et al.

Title: ELECTRONIC CIRCUIT DEVICE

3/22

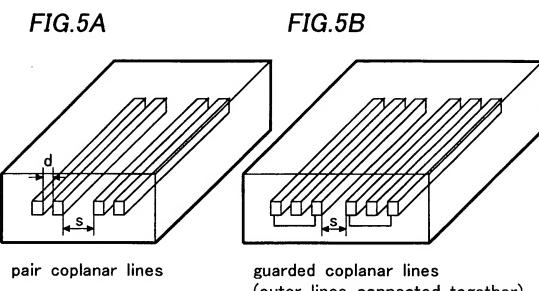
FIG.4



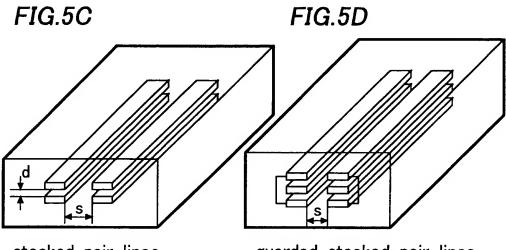
App No.: Not Yet Assigned Do Inventor: Kanji OTSUKA, et al.
Title: ELECTRONIC CIRCUIT DEVICE

Docket No.: 492322017000

4/22



(outer lines connected together)



stacked pair lines

guarded stacked pair lines

App No.: Not Yet Assigned Dinventor: Kanji OTSUKA, et al.
Title: ELECTRONIC CIRCUIT DEVICE

5/22

FIG.6

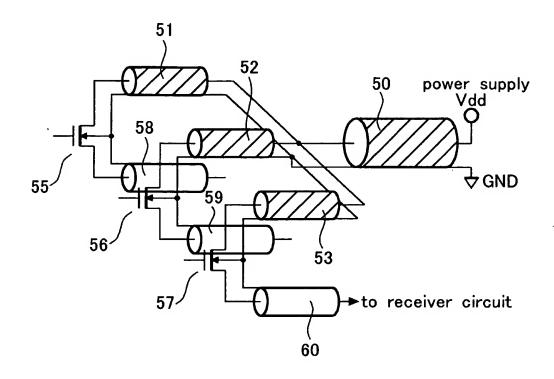
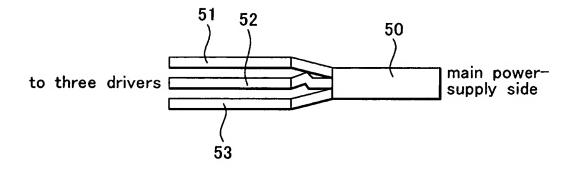


FIG.7



App No.: Not Yet Assigned De Inventor: Kanji OTSUKA, et al.
Title: ELECTRONIC CIRCUIT DEVICE Docket No.: 492322017000

6/22

FIG.8A

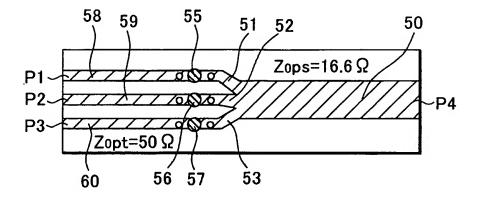
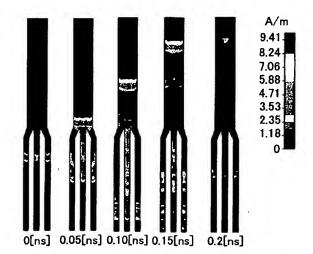


FIG.8B



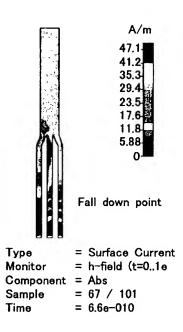
Туре = Surface Monitor = h-field Component = Abs Sample = 84 / 101 Time = 8.3e-010

simulation of progress of electromagnetic wave around confluence of power-supply/ground pair transmission lines

App No.: Not Yet Assigned De Inventor: Kanji OTSUKA, et al. Title: ELECTRONIC CIRCUIT DEVICE

7/22

FIG.9



simulation of progress electromagnetic when two of three driver transistors turn on ahead App No.: Not Yet Assigned Docket No.: 492322017000

Inventor: Kanji OTSUKA, et al.
Title: ELECTRONIC CIRCUIT DEVICE

8/22

FIG.10A

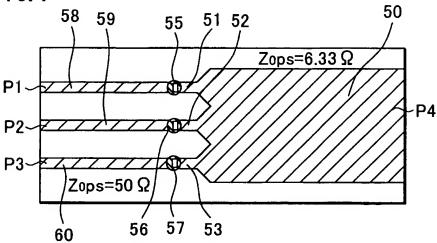
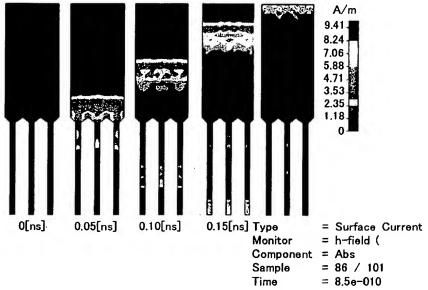


FIG.10B



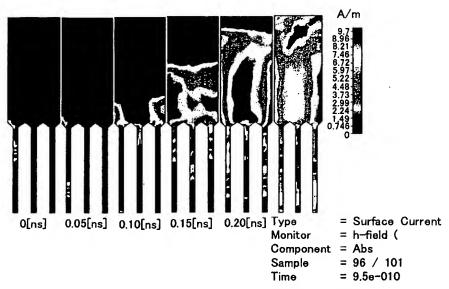
simulation of progress of electromagnetic wave around confluence of power-supply/ground pair transmission lines

App No.: Not Yet Assigned Docket No.: 492322017000

Inventor: Kanji OTSUKA, et al. Title: ELECTRONIC CIRCUIT DEVICE

9/22

FIG.11



simulation of progress of electromagnetic wave around confluence of power-supply/ground pair transmission lines

App No.: Not Yet Assigned

Docket No.: 492322017000

Inventor: Kanji OTSUKA, et al. Title: ELECTRONIC CIRCUIT DEVICE

10/22

FIG.12A

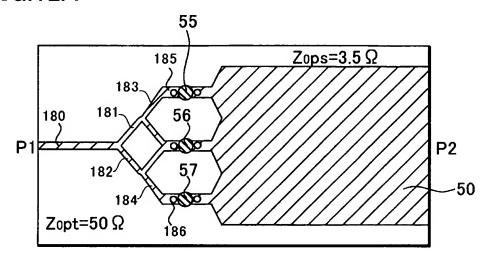
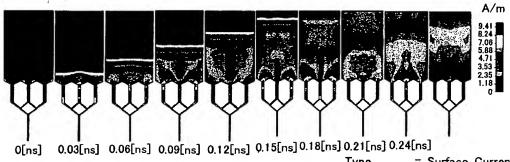


FIG.12B



Туре = Surface Current

Monitor = h-field (Component = Abs = 93 / 101 Sample = 9.2e-010Time

Docket No.: 492322017000 App No.: Not Yet Assigned Do Inventor: Kanji OTSUKA, et al.
Title: ELECTRONIC CIRCUIT DEVICE

11/22

FIG.13

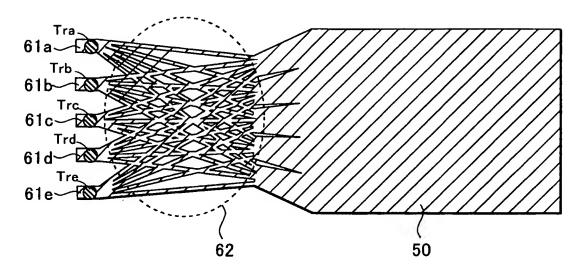
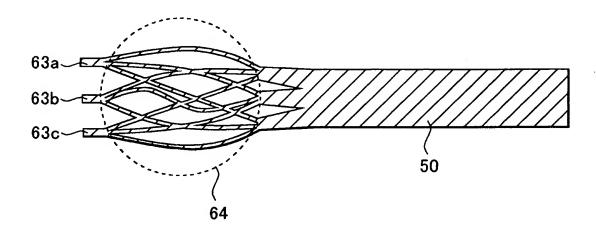


FIG.14



App No.: Not Yet Assigned Do Inventor: Kanji OTSUKA, et al.
Title: ELECTRONIC CIRCUIT DEVICE

12/22

FIG.15

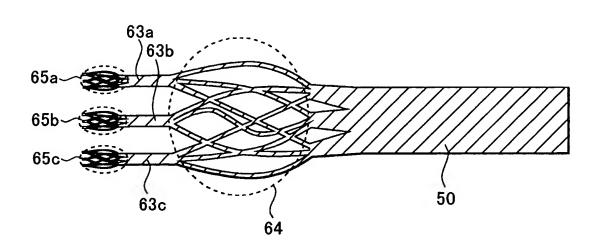
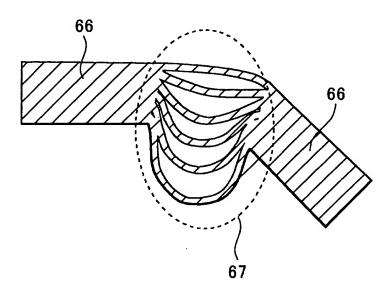


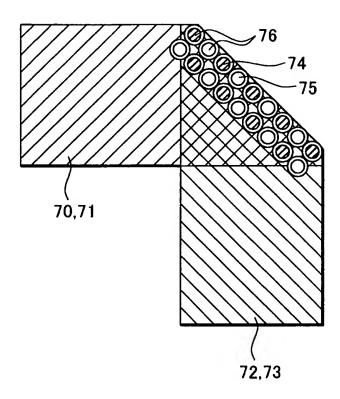
FIG.16



Docket No.: 492322017000 App No.: Not Yet Assigned Do Inventor: Kanji OTSUKA, et al.
Title: ELECTRONIC CIRCUIT DEVICE

13/22

FIG.17



App No.: Not Yet Assigned De Inventor: Kanji OTSUKA, et al. Title: ELECTRONIC CIRCUIT DEVICE

14/22

FIG.18A

FIG.18B

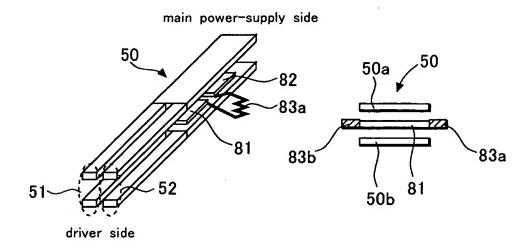
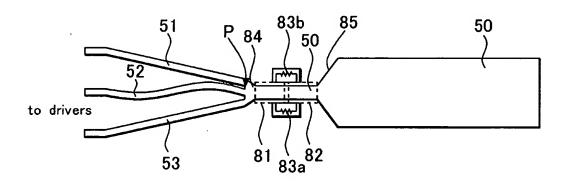


FIG.19



App No.: Not Yet Assigned Do Inventor: Kanji OTSUKA, et al.

Title: ELECTRONIC CIRCUIT DEVICE

15/22

FIG.20A

FIG.20B

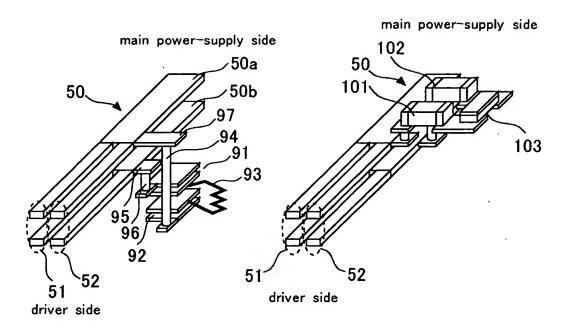
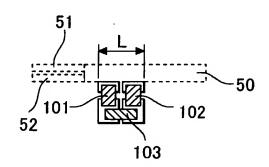


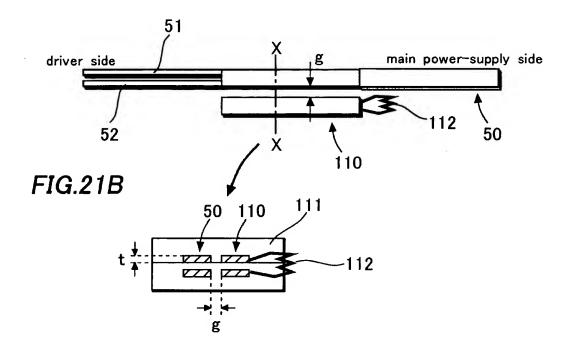
FIG.20C



App No.: Not Yet Assigned De Inventor: Kanji OTSUKA, et al.
Title: ELECTRONIC CIRCUIT DEVICE

16/22

FIG.21A



App No.: Not Yet Assigned De Inventor: Kanji OTSUKA, et al.

Title: ELECTRONIC CIRCUIT DEVICE

Docket No.: 492322017000

17/22

FIG.22

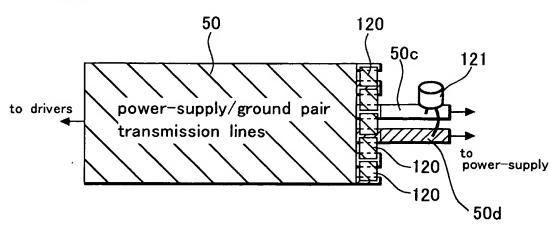


FIG.23A

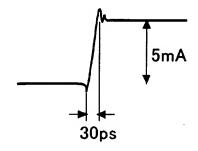


FIG.23B 5mA $30ps \times 10$ times or more

App No.: Not Yet Assigned Doc Inventor: Kanji OTSUKA, et al.

Title: ELECTRONIC CIRCUIT DEVICE

18/22

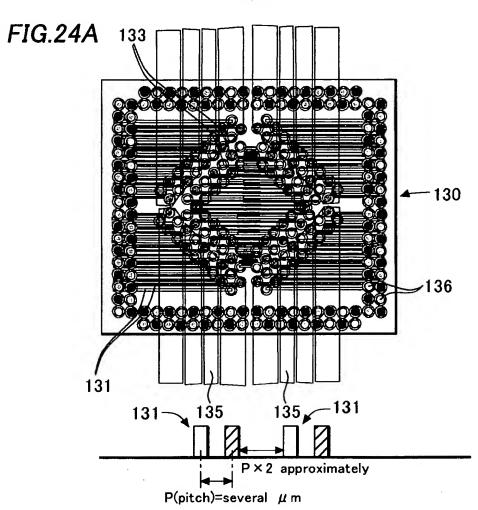
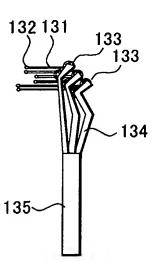


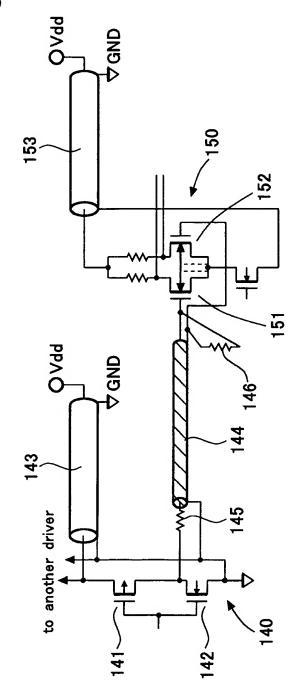
FIG.24B



App No.: Not Yet Assigned Do Inventor: Kanji OTSUKA, et al.
Title: ELECTRONIC CIRCUIT DEVICE

19/22

FIG.25



App No.: Not Yet Assigned Do Inventor: Kanji OTSUKA, et al.
Title: ELECTRONIC CIRCUIT DEVICE

20/22

FIG.26

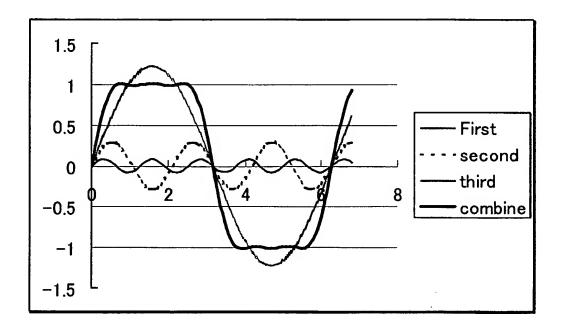
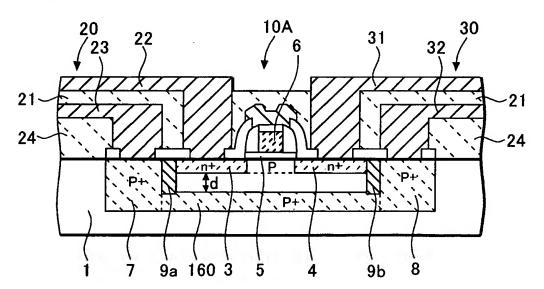
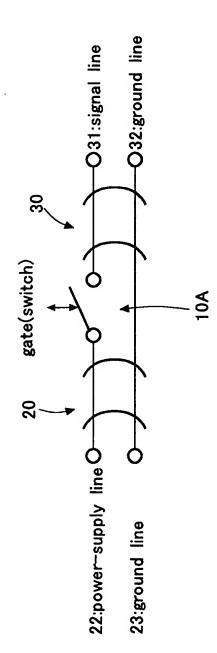


FIG.27



21/22

FIG.28



Docket No.: 492322017000 pAg: No.: Not Yet Assigned Do ■ Inwentor: Kanji OTSUKA, et al. ■ DE DLECTRONIC CIRCUIT DEVICE

22/22

FIG.29

